

JAPANESE INDUSTRIAL STANDARD

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JIS C 61000-3-2:2019

(IEEJ/JSA)

Electromagnetic compatibility (EMC) — Part 3-2: Limits — Limits for harmonic current emissions (equipment input current ≤ 20 A per phase)

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In the event of any doubts arising as to the contents, the original JIS is to be the final authority.

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Foreword

This Japanese Industrial Standard has been revised by the Minister of Economy, Trade and Industry through deliberations at the Japanese Industrial Standards Committee as the result of proposal for revision of Japanese Industrial Standard submitted by The Institute of Electrical Engineers of Japan (IEEJ)/Japanese Standards Association (JSA) with the draft being attached, based on the provision of Article 12 Clause 1 of the Industrial Standardization Law applicable to the case of revision by the provision of Article 14.

Consequently **JIS C 61000-3-2**:2011 is replaced with this Standard.

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Attention is drawn to the possibility that some parts of this Standard may conflict with patent rights, applications for a patent after opening to the public or utility model rights. The relevant Minister and the Japanese Industrial Standards Committee are not responsible for identifying any of such patent rights, applications for a patent after opening to the public or utility model rights.

JIS C 61000 series consists of the following 15 parts under the general title *Electromagnetic compatibility (EMC)*

- JIS C 61000-3-2 Part 3-2 : Limits Limits for harmonic current emissions (equipment input current ≤ 20 A per phase)
- JIS C 61000-4-2 Part 4-2: Testing and measurement techniques Electrostatic discharge immunity test
- JIS C 61000-4-3 Part 4-3: Testing and measurement techniques Radiated, radio-frequency, electromagnetic field immunity test
- JIS C 61000-4-4 Part 4-4: Testing and measurement techniques Electrical fast transient/burst immunity test
- JIS C 61000-4-5 Part 4-5: Testing and measurement techniques Surge immunity test
- JIS C 61000-4-6 Part 4-6: Testing and measurement techniques Immunity to conducted disturbances, induced by radio-frequency fields JIS C 61000-4-7 Part 4-7: Testing and measurement techniques General guide on harmonics and interharmonics measurements and instrumentation, for power supply systems and equipment connected thereto
- JIS C 61000-4-8 Part 4-8: Testing and measurement techniques Power frequency magnetic field immunity test
- JIS C 61000-4-11 Part 4-11: Testing and measuring techniques Voltage dips, short interruptions and voltage variations immunity tests JIS C 61000-4-16 Part 4-16: Testing and measurement techniques Test for immunity to conducted, common mode disturbances in the frequency range 0 Hz to 150 kHz
- JIS C 61000-4-20 Part 4-20: Testing and measurement techniques Emission and immunity testing in transverse electromagnetic (TEM) waveguides
- JIS C 61000-4-22 Part 4-22: Testing and measurement techniques— Radiated emissions and immunity measurements in fully anechoic rooms (FARs)
- JIS C 61000-4-34 Part 4-34: Testing and measurement techniques Voltage dips, short interruptions and voltage variations immunity tests for equipment with mains current more than 16 A per phase
- JIS Ĉ 61000-6-1 Part 6-1: Generic standards Immunity standard for residential, commercial and light-industrial environments
- JIS C 61000-6-2 Part 6-2: Generic standards Immunity standard for industrial environments

Electromagnetic compatibility (EMC) — Part 3-2: Limits — Limits for harmonic current emissions (equipment input current ≤ 20 A per phase)

JIS C 61000-3-2:2019

Introduction

This Japanese Industrial Standard has been prepared based on **IEC 61000-3-2**: 2018, Edition 5, with some modifications of the technical contents, taking into consideration of e.g. the difference of electric power systems.

The vertical lines on both sides and dotted underlines indicate changes from the corresponding International Standard. A list of modifications with the explanations is given in Annex JA.

1 Scope

This Standard specifies the limitation of harmonic currents injected into the public electricity supply system.

It specifies limits of harmonic components of the input current which can be produced by equipment tested under specified conditions.

This Standard is applicable to electrical and electronic equipment (household electrical appliances and general-purpose products) (hereafter referred to as equipment) having a rated input current up to and including 20 A per phase, and intended to be connected to public electricity supply systems having a voltage not more than 300 V. However, being outside the ranges given above does not preclude the application of this Standard.

Arc welding equipment which is not professional equipment, with a rated input current up to and including <u>20 A</u> per phase, is included in the scope of this Standard. Arc welding equipment intended for professional use, as specified in **JIS C 9300-1**, is excluded from the scope of this Standard.

The tests according to this Standard are type tests.

NOTE The International Standard corresponding to this Standard and the symbol of degree of correspondence are as follows.

IEC 61000-3-2: 2018 Electromagnetic compatibility (EMC) — Part 3-2: Limits — Limits for harmonic current emissions (equipment input current \leq 16 A per phase) (MOD)

In addition, symbols which denote the degree of correspondence in the contents between the relevant International Standards and **JIS** are IDT (identical), MOD (modified), and NEQ (not equivalent) according to **ISO/IEC Guide 21-1**.