

# JEITA

電子情報技術産業協会規格

Standard of Japan Electronics and Information Technology Industries Association

## **JEITA ED - 7716**

半導体ソケット個別規格オープントップ  
(メモリ用 FBGA)

**Standard of open-top type socket**

**[ Fine-pitch Ball Grid Array (FBGA) for Memory IC ]**

2006年3月制定

作 成

半導体パッケージ標準化委員会

Technical Standardization Committee on Semiconductor Device Package

発 行

社団法人 電子情報技術産業協会

Japan Electronics and Information Technology Industries Association

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## 登録一覧表

## Registration Table

半導体ソケット名称 : SFB (0.80mm pitch)  
Socket Name

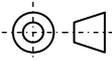
対象パッケージ : FBGA (0.8mm pitch)  
Object Package

登録番号 : XXX-XXXX-001  
Registration No.

呼び寸法 E x D	端子直線間隔 e Terminal pitch	
	0.80	
	Package	Socket
6.00 x 8.00	48-001-AC 48-001-AD 48-001-AE	48-001-AC 48-001-AD 48-001-AE
6.00 x 10.00	32-001-BA	32-001-BA
10.00 x 8.00	56-001-CA	56-001-CA
8.00 x 8.00	64-001-DA	64-001-DA
9.00 x 9.00	65-001-EA	65-001-EA
8.00 x 10.00	68-001-FA	68-001-FA
11.00 x 8.00	72-001-GA	72-001-GA(1) 72-001-GA(2)
8.00 x 11.00	73-001-HA	73-001-HA(1) 73-001-HA(2)
9.00 x 11.00	73-001-HB	73-001-HB
9.00 x 12.00	73-001-HC	73-001-HC(1) 73-001-HC(2)
9.00 x 14.00	77-001-IA	77-001-IA

注 表中の数値は、「(端子数)-(連番)-(整理番号)」を示す。

Note: Numbers in the Table Indicate "(Terminal number n)-(Consecutive number)-(Serial number)"

	JEITA STANDARD IC SOCKET OUTLINE DRAWING	DATE 05-XX-XX	SHEET 1/
IC SOCKET NAME SFB		JEITA REGISTRATION NO. XX-XXXX-001	